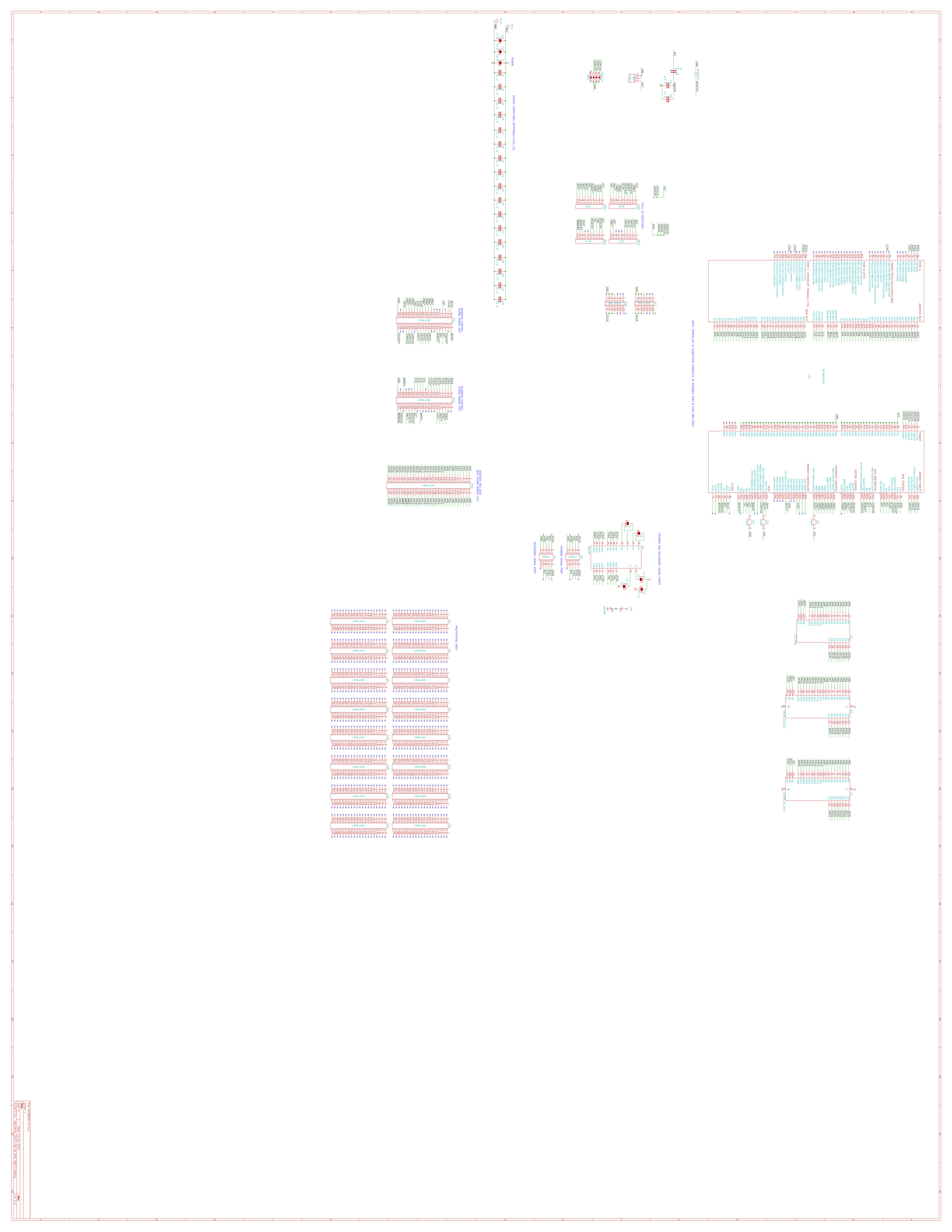
BYPASS CAPACITORS (OPTIONALLY SOLD)
POWER
PULL UP RESISTORS
4.7K
10K
NOTE: PREFILLED O SUBSYSTEM CONNECTS TO MCA9500 PORT B PIN PB0-PB14.
ACCESSIBLE DIO
PORT A
PORT B (LATCHED)
PORT C (LATCHED)
DATA BUS (D)
ADDRESS BUS
MCA9500
U12
MEMORY
SYSTEM CONTROL
SERIAL PORT
PARALLEL PORT
PRIMARY AND SECONDARY SERIAL PORTS
MCA9500 CONTROL
SERIAL BUS (SB)
MCA9500 DATA
SERIAL BUS (SB)
MCA9500 ADDRESS
SERIAL BUS (SB)
MCA9500 DATA
PARALLEL BUS (PB)
PORT C
PORT B
PORT A
CONN_20X2
SECONDARY SERIAL PORT
PRIMARY SERIAL PORT
SERIAL
U1
U2
U3
U4
U5
U6
U7
U8
U9
U10
U11
U12
U13
U14
U15
U16
U17
U18
U19
U20
U21
U22
U23
U24
U25
U26
U27
U28
U29
U30
U31
U32
U33
U34
U35
U36
U37
U38
U39
U40